

PS9113

1 Mbps, OPEN COLLECTOR OUTPUT HIGH CMR, INTELLIGENT POWER MODULE 5-PIN SOP (SO-5) PHOTOCOUPLER R08DS0124EJ0300 Rev.3.00 Jan 28, 2016

Description

The PS9113 is an optically coupled isolator containing a GaAlAs LED on the input side and a photo diode and a signal processing circuit on the output side on one chip.

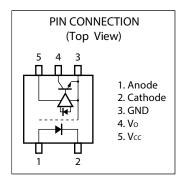
The PS9113 is specified high CMR, high CTR and pulse width distortion with operating temperature. It is suitable for IPM drive.

Features

- High instantaneous common mode rejection voltage (CM_H, CM_L = ± 15 kV/ μ s MIN.)
- Small package (SO-5)
- High-speed response ($t_{PHL} = 500 \text{ ns MAX.}$, $t_{PLH} = 750 \text{ ns MAX.}$)
- Maximum propagation delays ($t_{PLH} t_{PHL} = 270 \text{ ns TYP.}$)
- Pulse width distortion ($|t_{PHL} t_{PLH}| = 270 \text{ ns TYP.}$)
- High isolation voltage (BV = 3 750 Vr.m.s.)
- Open collector output
- Ordering number of taping product: PS9113-F3: 2 500 pcs/reel
- Pb-Free product

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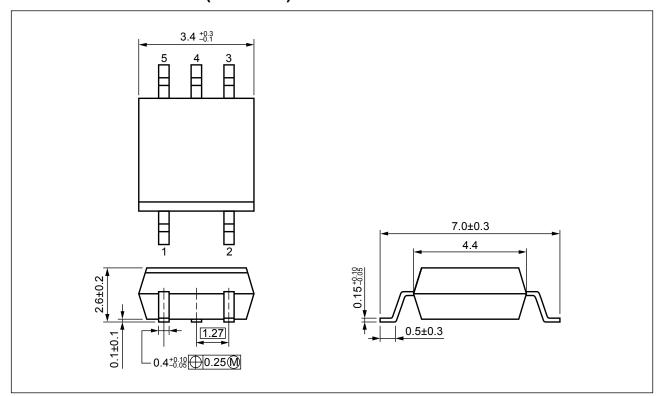
- Safety standards
 - UL approved: UL1577, Single protection
 - CSA approved: CA5A, CAN/CSA-C22.2 60065 & 60950-1, Basic insulation
 - VDE approved: DIN EN 60747-5-5 (Option)



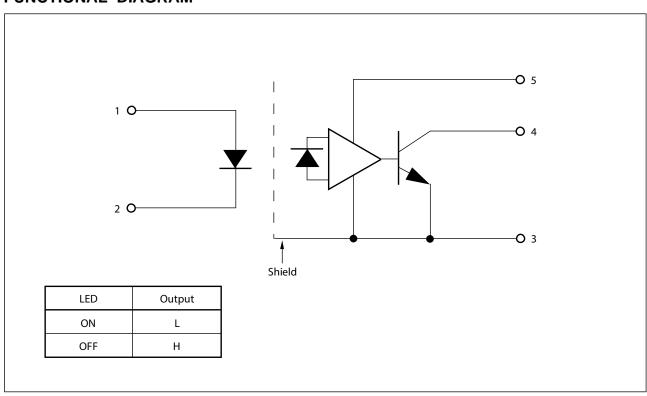
APPLICATIONS

- IPM Driver
- General purpose inverter

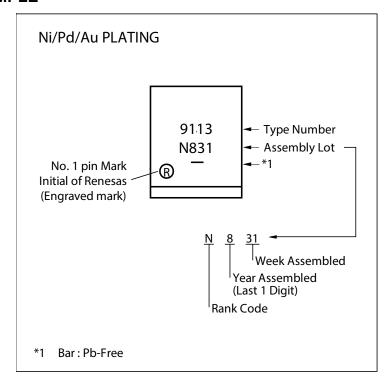
PACKAGE DIMENSIONS (UNIT: mm)



FUNCTIONAL DIAGRAM



MARKING EXAMPLE



<R> ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number ^{*1}
PS9113	PS9113-AX	Pb-Free	20 pcs (Tape 20 pcs cut)	Standard products	PS9113
PS9113-F3	PS9113-F3-AX	(Ni/Pd/Au)	Embossed Tape 2500 pcs/reel	(UL, CSA approved)	
PS9113-V	PS9113-V-AX		20 pcs (Tape 20 pcs cut)	UL, CSA approved DIN EN 60747-5-5	
PS9113-V-F3	PS9113-V-F3-AX		Embossed Tape 2 500 pcs/reel	(VDE0884-5) approved (Option)	

Notes*: 1. For the application of the Safety Standard, following part number should be used.

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C, unless otherwise specified)

Parameter		Symbol	Ratings	Unit
Diode	Forward Current*1	l _F	25	mA
	Reverse Voltage	VR	5	V
Detector	Supply Voltage	Vcc	−0.5 to +25	V
	Output Voltage	Vo	−0.5 to +25	V
	Output Current	lo	15	mA
	Power Dissipation*2	Pc	100	mW
Isolation Voltage*3		BV	3 750	Vr.m.s.
Operating Ambient Temperature		TA	-40 to +100	°C
Storage Temperature		T _{stg}	−55 to +125	°C

Notes*: 1. Reduced to 0.33 mA/°C at T_A = 70°C or more.

- 2. Reduced to 1.9 mW/ $^{\circ}$ C at T_A = 70 $^{\circ}$ C or more.
- 3. AC voltage for 1 minute at T_A = 25°C, RH = 60% between input and output. Pins 1-2 shorted together, 3-5 shorted together.

RECOMMENDED OPERATING CONDITIONS

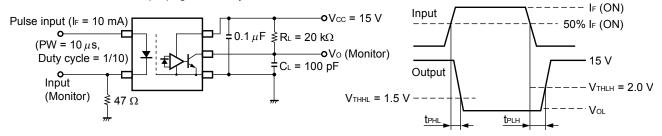
Parameter	Symbol	MIN.	TYP.	MAX.	Unit
High Level Input Current	I _{FH}	10		20	mA
Output Voltage	Vo	0		20	V
Supply Voltage	Vcc	4.5	15	20	V
LED Off Voltage	VF	0		0.8	V

ELECTRICAL CHARACTERISTICS ($T_A = -40 \text{ to } +100^{\circ}\text{C}, \ V_{CC} = 15 \ \text{V}, \ unless otherwise specified)$

Parameter		Symbol	Conditions	MIN.	TYP. ^{*1}	MAX.	Unit
Diode	Forward Voltage	V_{F}	I _F = 10 mA	1.3	1.65	2.1	V
	Reverse Current	I _R	V _R = 3 V			200	μA
	Terminal Capacitance	Ct	V = 0 V, f = 1 MHz, T _A = 25°C		30		pF
Detector	Low Level Output Voltage	V _{OL}	I _F = 10 mA, I _{OL} = 2.4 mA		0.13	0.6	V
	High Level Output Current	I _{OH}	$V_{CC} = V_O = 20 \text{ V}, V_F = 0.8 \text{ V}$		0.01	50	μA
	High Level Supply Current	I _{CCH}	V_{CC} = 20 V, V_F = 0.8 V, V_O = open		0.6	1.3	mA
	Low Level Supply Current	I _{CCL}	V_{CC} = 20 V, I_F = 10 mA, V_O = open		0.6	1.3	mA
Coupled	Threshold Input Current $(H \rightarrow L)$	I _{FHL}	V _O = 0.8 V, I _O = 0.75 mA		1.5	5.0	mA
	Current Transfer Ratio (I _C /I _F)	CTR	I _F = 10 mA, V _O = 0.6 V	44	110		%
	Isolation Resistance	R _{I-O}	$V_{I-O} = 1 \text{ kV}_{DC}, \text{ RH} = 40 \text{ to } 60\%,$ $T_A = 25^{\circ}\text{C}$	10 ¹¹			Ω
	Isolation Capacitance	C _{I-O}	V = 0 V, f = 1 MHz, TA = 25°C		0.6		pF
	Propagation Delay Time $(H \rightarrow L)^{*2}$	t _{PHL}	I_F = 10 mA, R_L = 20 k Ω , C_L = 100 pF, V_{THHL} = 1.5 V, V_{THLH} = 2.0 V		250	500	ns
	Propagation Delay Time $(L \rightarrow H)^{*2}$	t _{PLH}			520	750	
	Maximum Propagation Delays	t _{PLH} —t _{PHL}		-200	270	650	
	Pulse Width Distortion (PWD)*2	t _{PHL} -t _{PLH}			270	650	
	Common Mode Transient Immunity at High Level Output*3	С _{мн}	$T_A = 25^{\circ}\text{C}$, $I_F = 0$ mA, $V_O > 3.0$ V, $V_{CM} = 1.5$ kV, RL = 20 k Ω , $C_L = 100$ pF	15			kV/μs
	Common Mode Transient Immunity at Low Level Output*3	Смь	$\begin{split} T_{A} &= 25^{\circ}\text{C}, \ I_{F} = 10 \ \text{mA}, \ V_{O} < 1.0 \ \text{V}, \\ V_{CM} &= 1.5 \ \text{kV}, \ R_{L} = 20 \ \text{k}\Omega, \\ C_{L} &= 100 \ \text{pF} \end{split}$	15			kV/μs

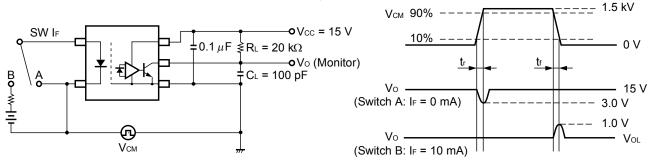
Notes*: 1. Typical values at $T_A = 25$ °C.

2. Test circuit for propagation delay time



C∟ includes probe and stray wiring capacitance.

3. Test circuit for common mode transient immunity

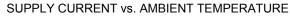


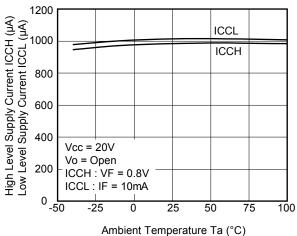
C∟ includes probe and stray wiring capacitance.

USAGE CAUTIONS

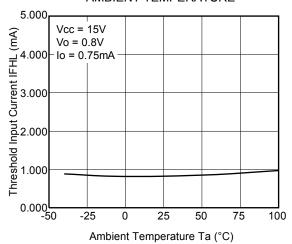
- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- 2. By-pass capacitor of $0.1~\mu F$ is used between V_{CC} and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10~mm.
- 3. Avoid storage at a high temperature and high humidity.

TYPICAL CHARACTERISTICS (T_A = 25°C unless otherwise specified)

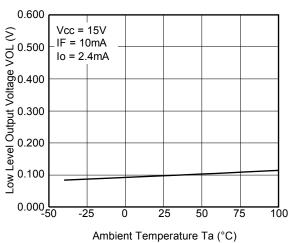




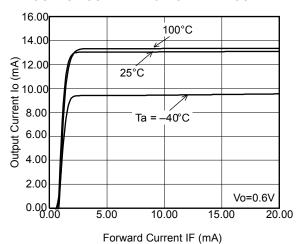
THRESHOLD INPUT CURRENT vs. AMBIENT TEMPERATURE



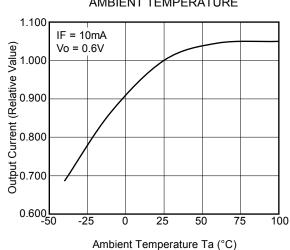
LOW LEVEL OUTPUT VOLTAGE vs. AMBIENT TEMPERATURE



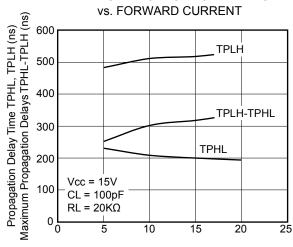
OUTPUT CURRENT vs. FORWARD CURRENT

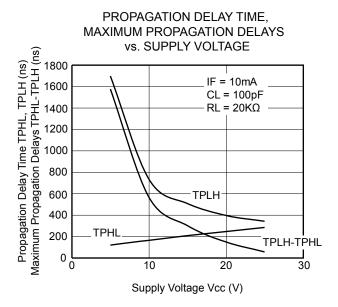


OUTPUT CURRENT vs. AMBIENT TEMPERATURE

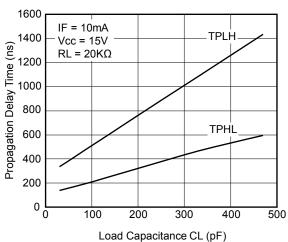


PROPAGATION DELAY TIME,
MAXIMUM PROPAGATION DELAYS

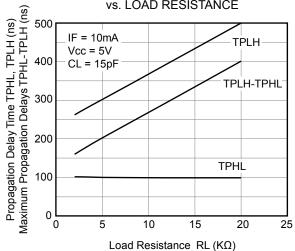




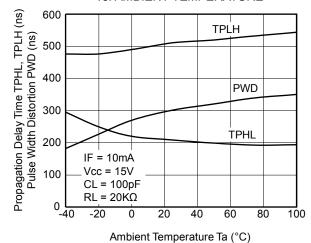




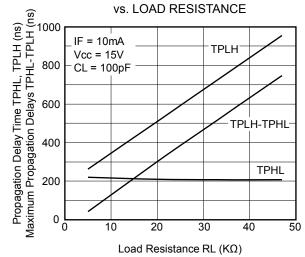
PROPAGATION DELAY TIME, MAXIMUM PROPAGATION DELAYS vs. LOAD RESISTANCE



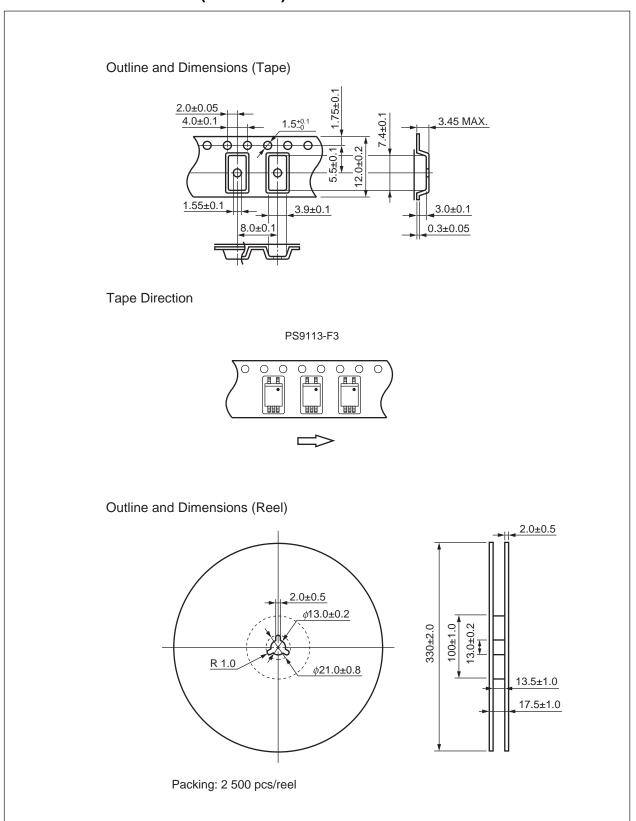
PROPAGATION DELAY TIME, PULSE WIDTH DISTORTION vs. AMBIENT TEMPERATURE



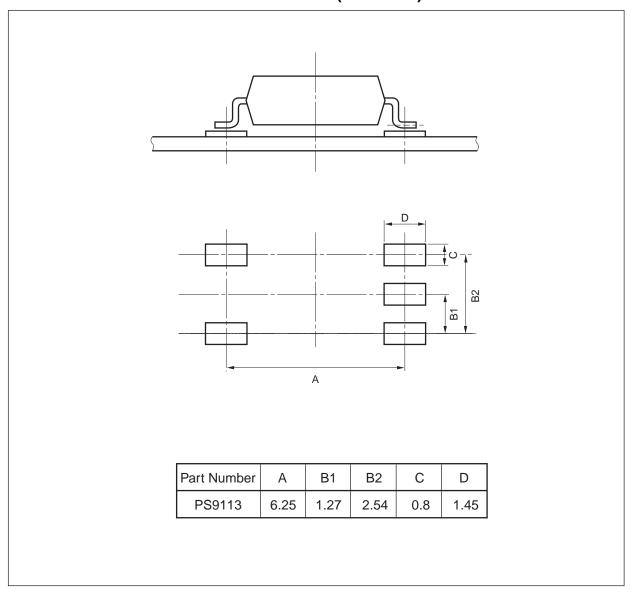
PROPAGATION DELAY TIME, MAXIMUM PROPAGATION DELAYS



<R> TAPING SPECIFICATIONS (UNIT: mm)



<R> RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



NOTES ON HANDLING

- 1. Recommended soldering conditions
 - (1) Infrared reflow soldering

• Peak reflow temperature 260°C or below (package surface temperature)

Time of peak reflow temperature
 Time of temperature higher than 220°C
 60 seconds or less

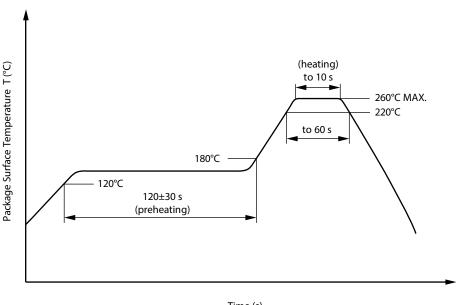
Time to preheat temperature from 120 to 180°C 120±30 s
 Number of reflows Three

• Flux Rosin flux containing small amount of chlorine (The flux

with a maximum chlorine content of 0.2 Wt% is

recommended.)

Recommended Temperature Profile of Infrared Reflow



Time (s)

(2) Wave soldering

• Temperature 260°C or below (molten solder temperature)

• Time 10 seconds or less

• Preheating conditions 120°C or below (package surface temperature)

• Number of times One (Allowed to be dipped in solder including plastic mold portion.)

• Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine

content of 0.2 Wt% is recommended.)

(3) Soldering by Soldering Iron

Peak Temperature (lead part temperature) 350°C or below
 Time (each pins) 3 seconds or less

• Flux Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

- (a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead
- (b) Please be sure that the temperature of the package would not be heated over 100°C

(4) Cautions

Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collectoremitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

USAGE CAUTIONS

- 1. Protect against static electricity when handling.
- 2. Avoid storage at a high temperature and high humidity.

SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Speck	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		40/100/21	
Dielectric strength maximum operating isolation voltage			
Test voltage (partial discharge test, procedure a for type test and	U _{IORM}	707	V_{peak}
random test)	Upr	1131	V_{peak}
$U_{pr} = 1.6 \times U_{IORM}$, Pd < 5 pC			P
Test voltage (partial discharge test, procedure b for all devices) U_{pr} = 1.875 × U_{IORM} , Pd < 5 pC	Upr	1 326	V _{peak}
Highest permissible overvoltage	U _{TR}	6 000	V_{peak}
Degree of pollution (DIN EN 60664-1 VDE 0110 Part 1)		2	
Comparative tracking index ((IEC 60112/DIN EN 60112 (VDE 0303 Part 11))	СТІ	175	
Material group (DIN EN 60664-1 VDE 0110 Part 1)		III a	
Storage temperature range	Tstg	-55 to +125	°C
Operating temperature range	TA	-40 to +100	°C
Isolation resistance, minimum value			
V _{IO} = 500 V dc at T _A = 25°C	Ris MIN.	1012	Ω
V _{IO} = 500 V dc at T _A MAX. at least 100°C	Ris MIN.	1011	Ω
Safety maximum ratings (maximum permissible in case of fault, see			
thermal derating curve)			
Package temperature	Tsi	150	°C
Current (input current I _F , Psi = 0)	lsi	200	mA
Power (output or total power dissipation)	Psi	300	mW
Isolation resistance			
V _{IO} = 500 V dc at T _A = Tsi	Ris MIN.	109	Ω

Caution

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
 - Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or i any way allow it to enter the mouth.

Revision History

PS9113 Data Sheet

		Description		
Rev.	Date	Page	Summary	
2.00	Apr 13, 2015	-	2nd edition issued	
3.00	Jan 28, 2016	p.1	FEATURES: Deletion of F4	
			FEATURES: Update of Safety standards	
		p.4 ORDERING INFORMATION: Deletion of F4		
			ORDERING INFORMATION: Update of Safety Standard Approval	
		p.9	TAPING SPECIFICATIONS: Deletion of F4	
		p.10	Modification of RECOMMENDED MOUNT PAD DIMENSIONS	

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